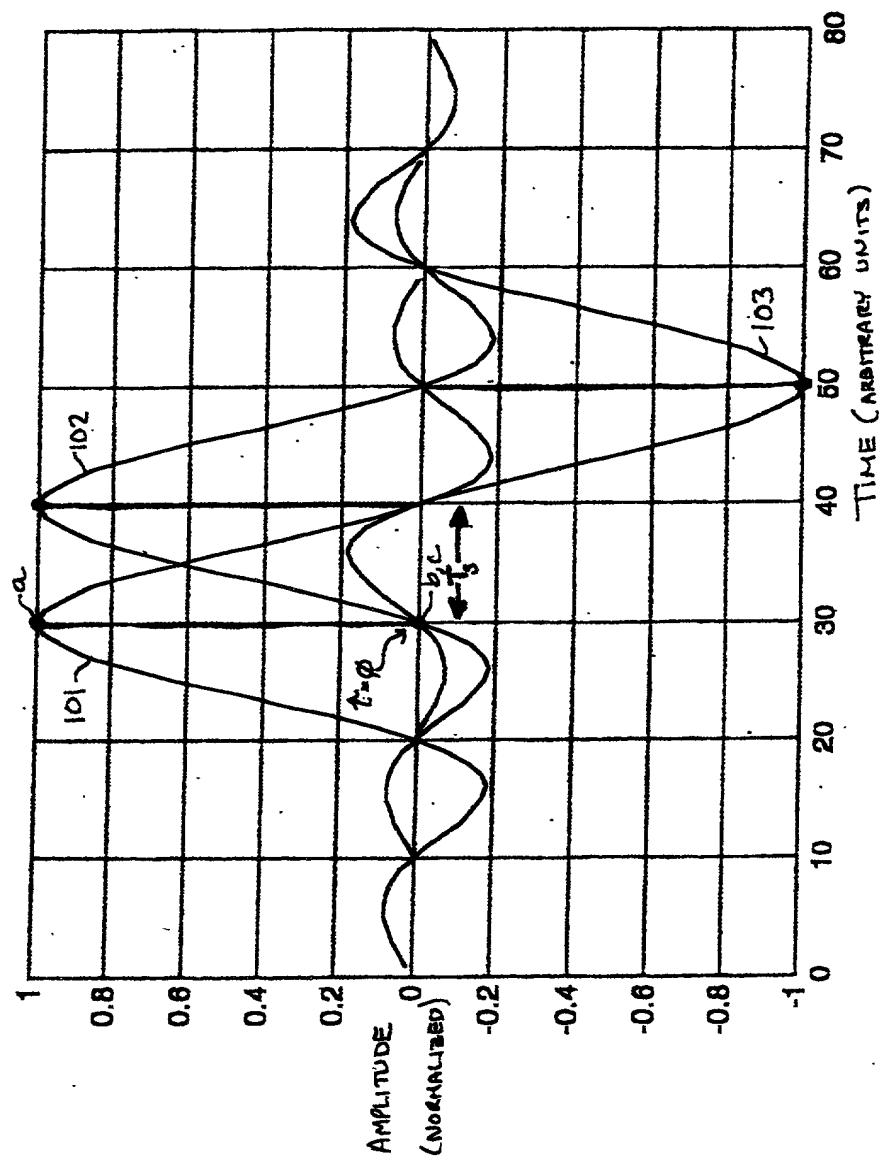
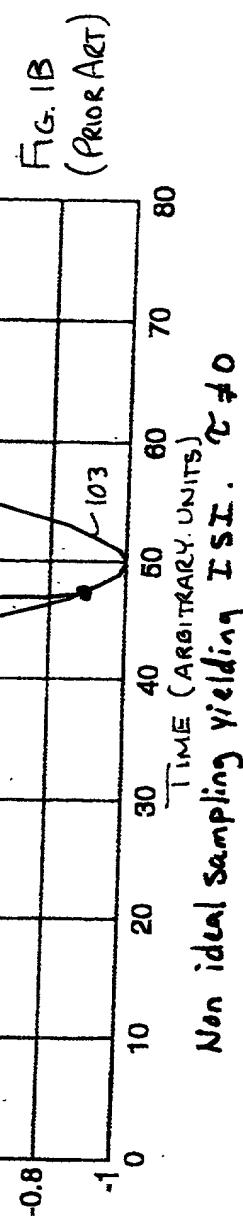


Fig. 1A  
(prior AET)





*Non ideal Sampling yielding ISI.  $\tau \neq 0$*

200 201 202 203 204 205 206

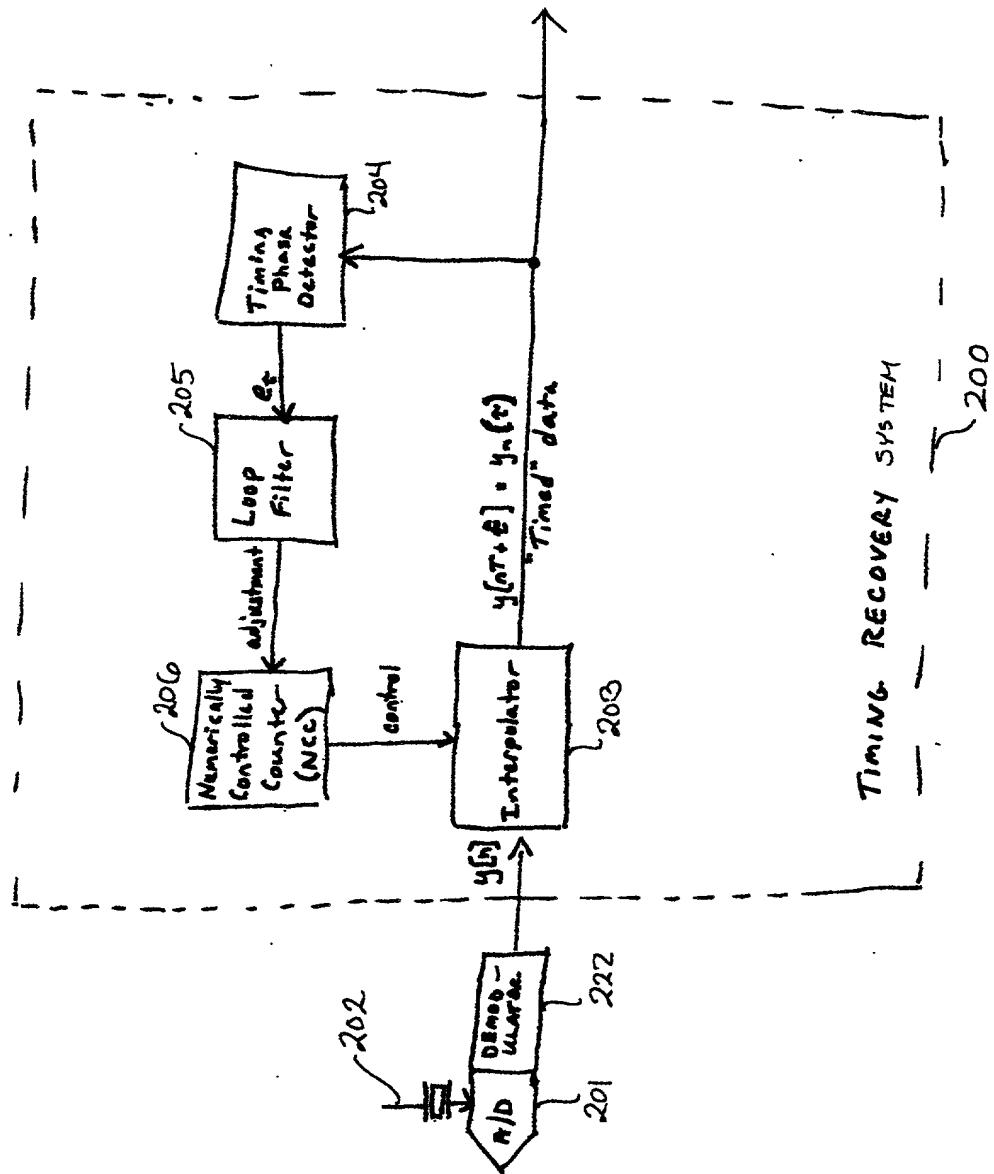


FIG. 2 (Prior Art)

Fig. 3 shows a Timing Recovery System.

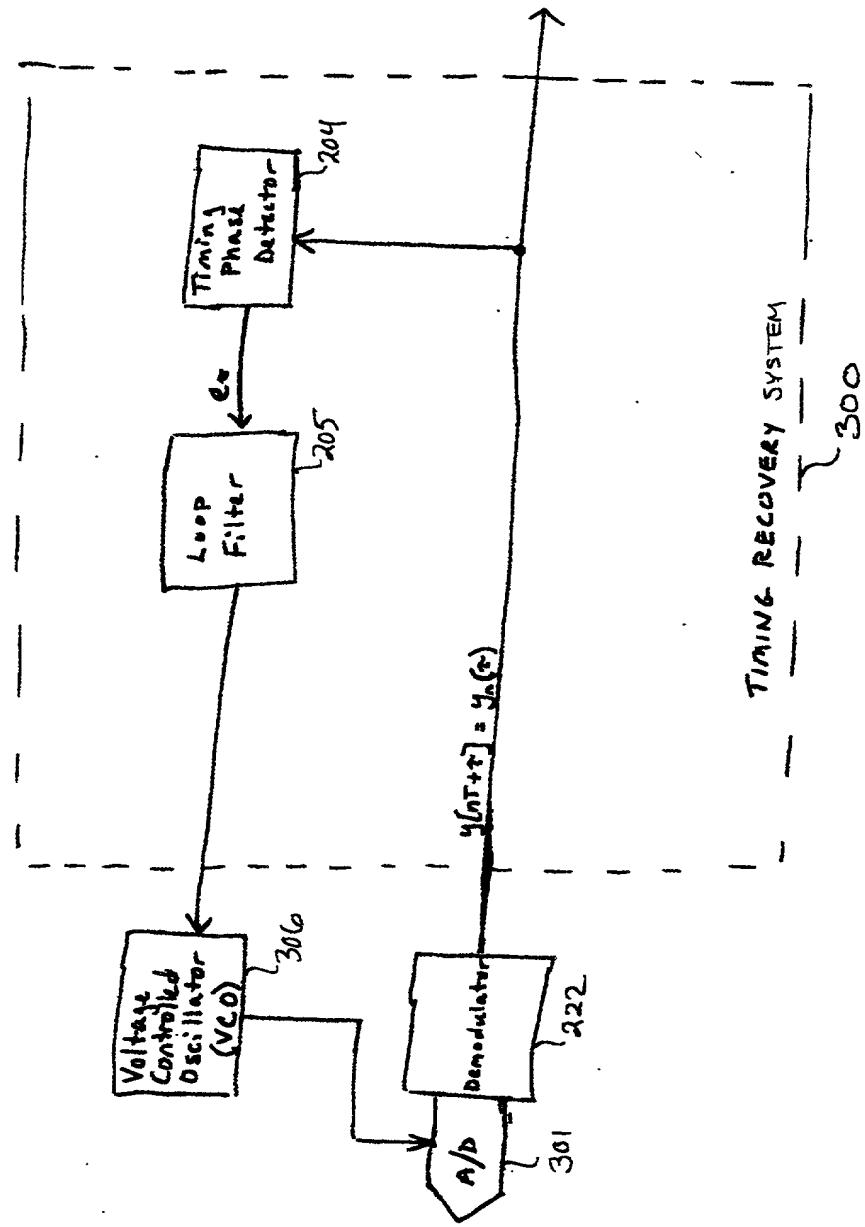
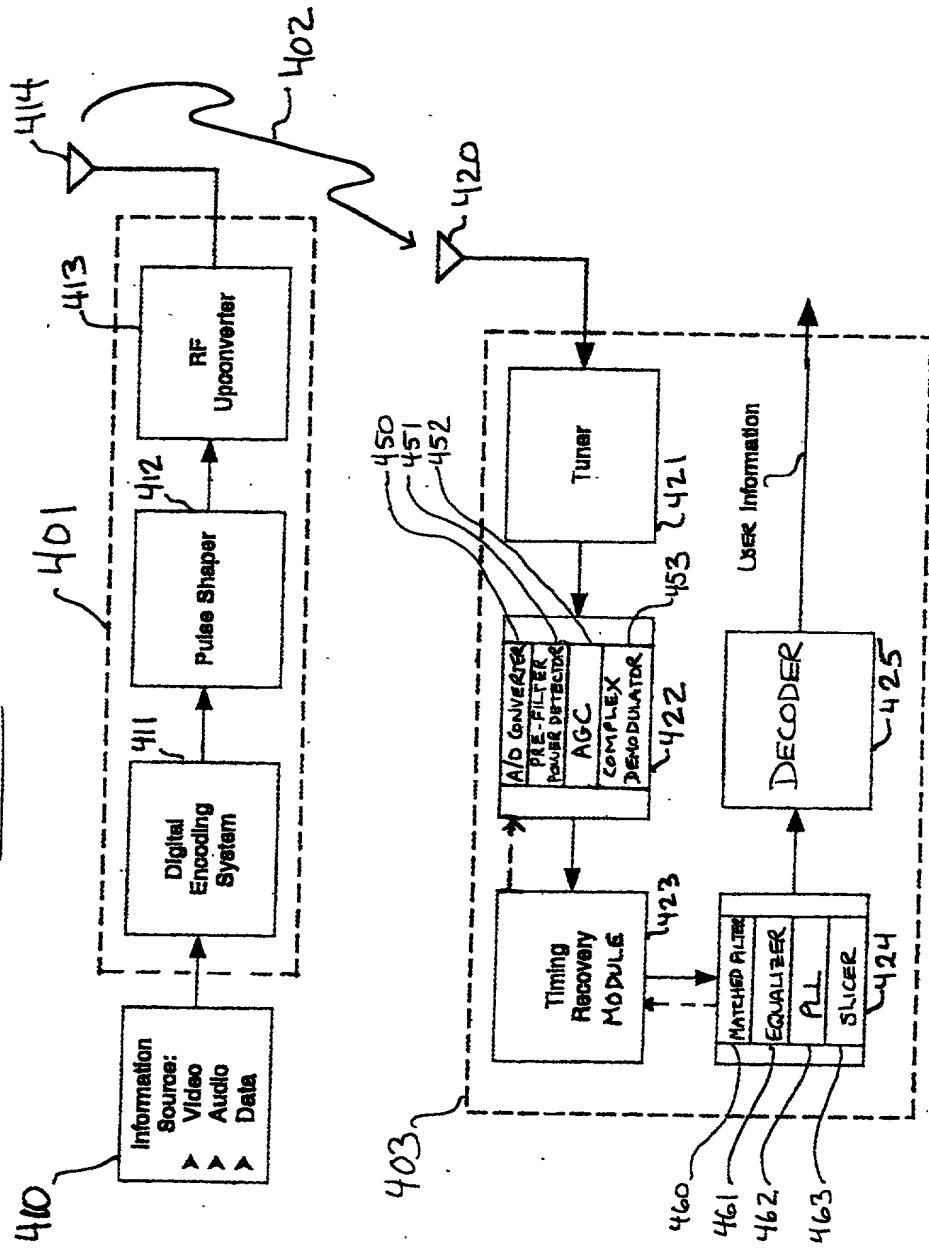


FIG. 3  
(Prior ART)

400

FIG. 4



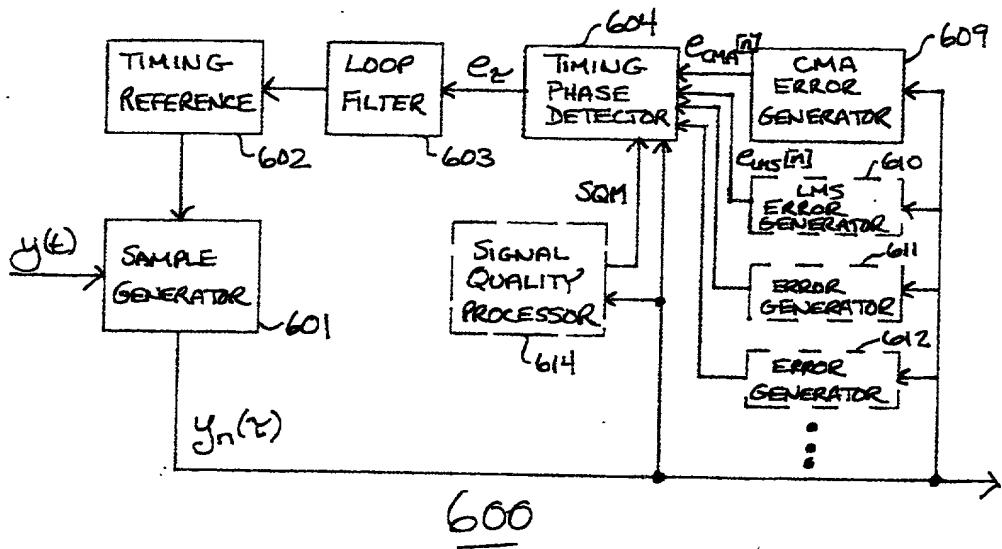
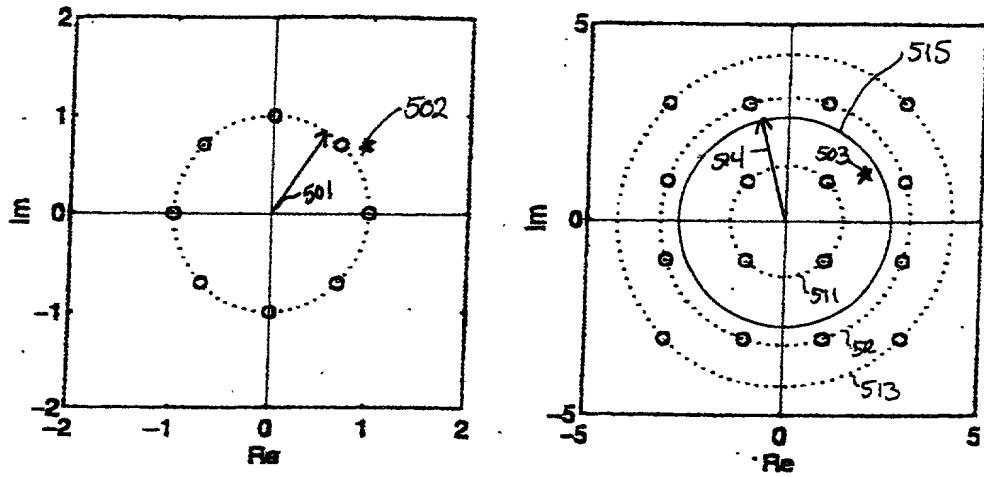
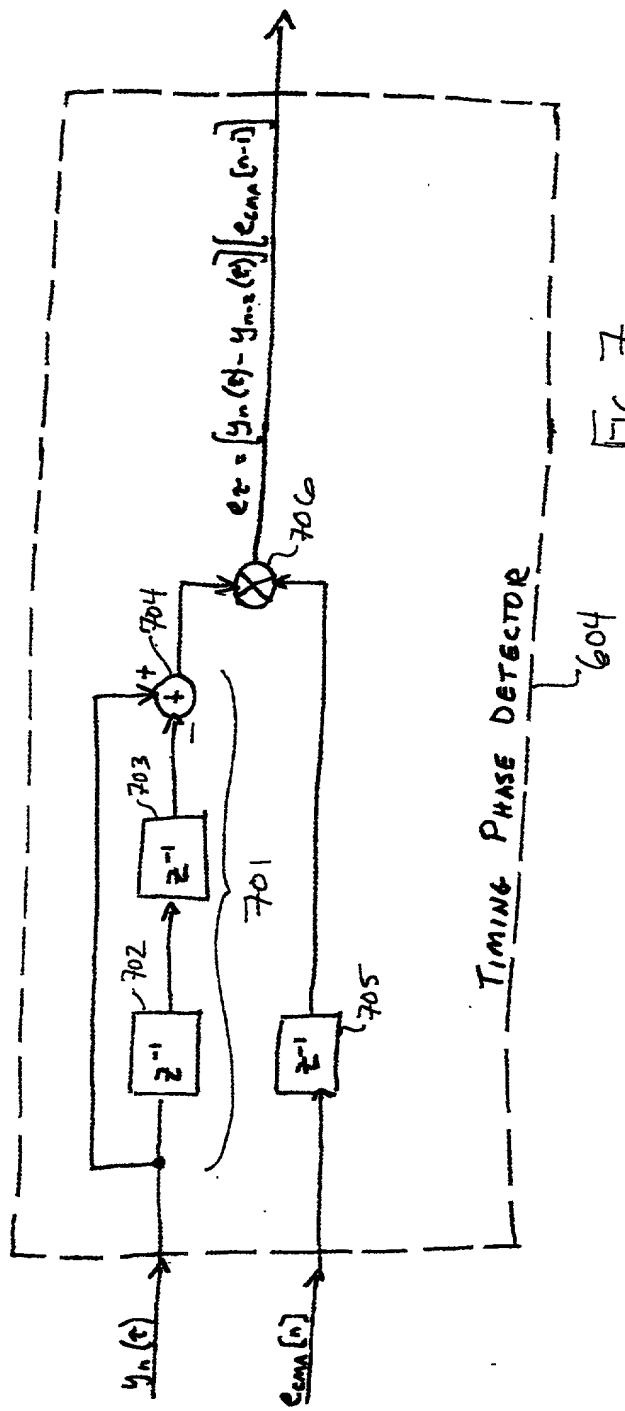


Fig. 7



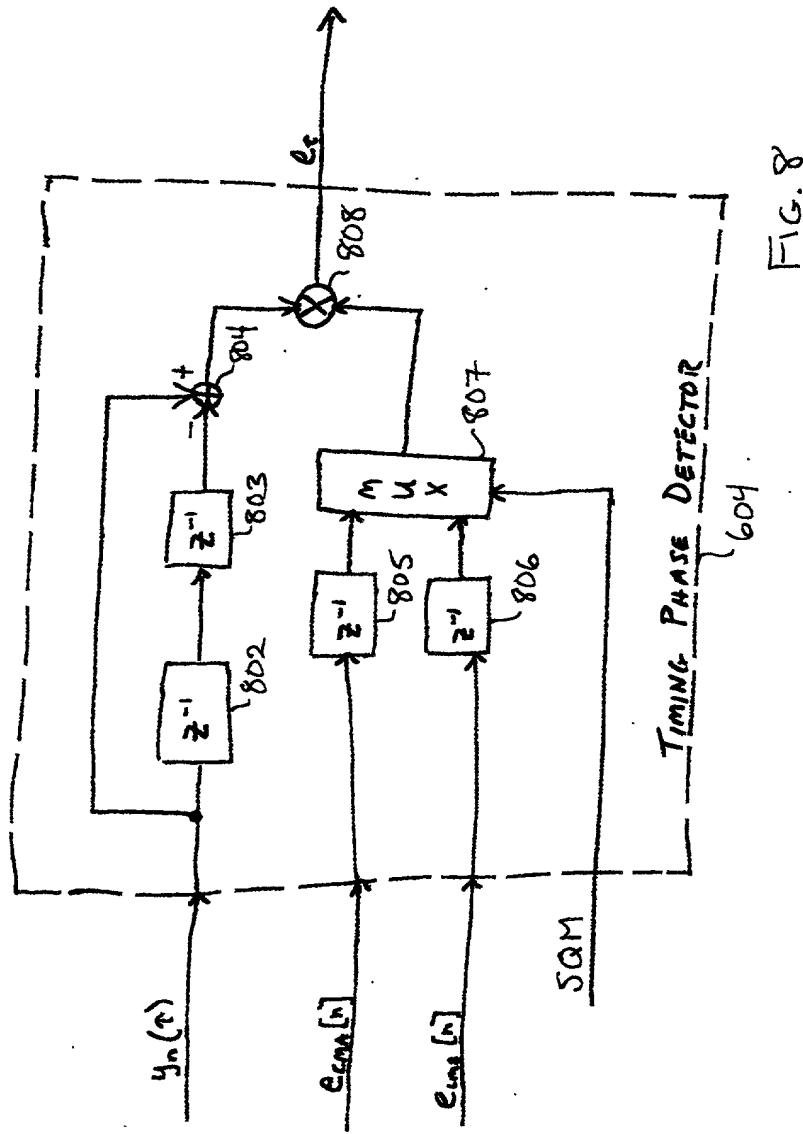


FIG. 8

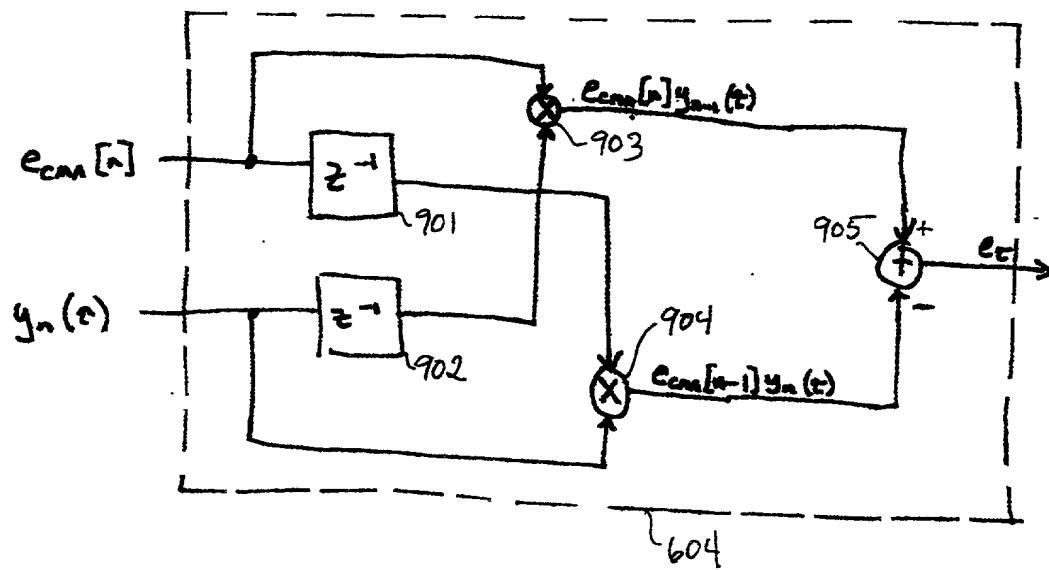


FIG. 9

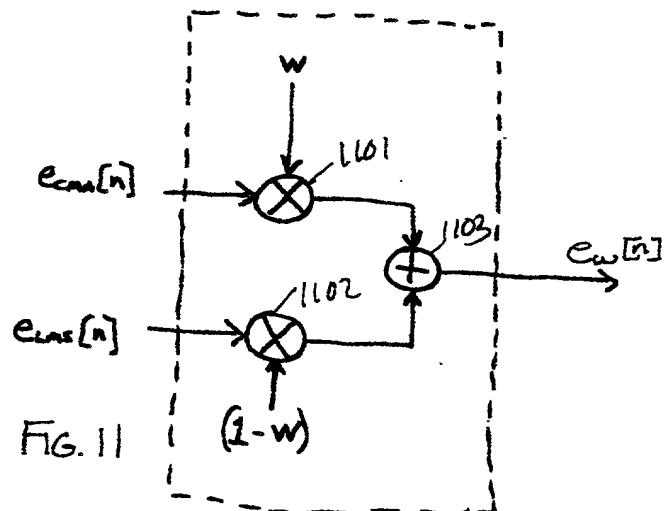
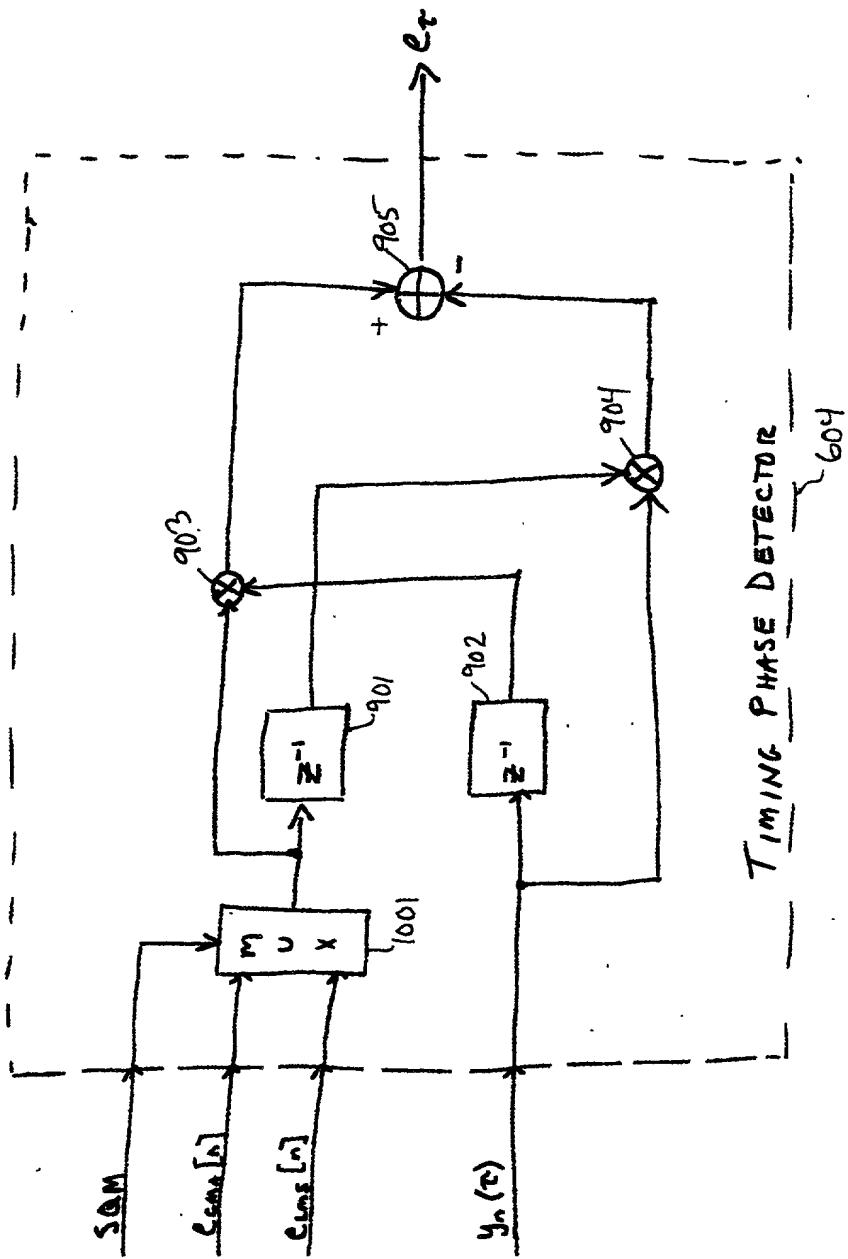
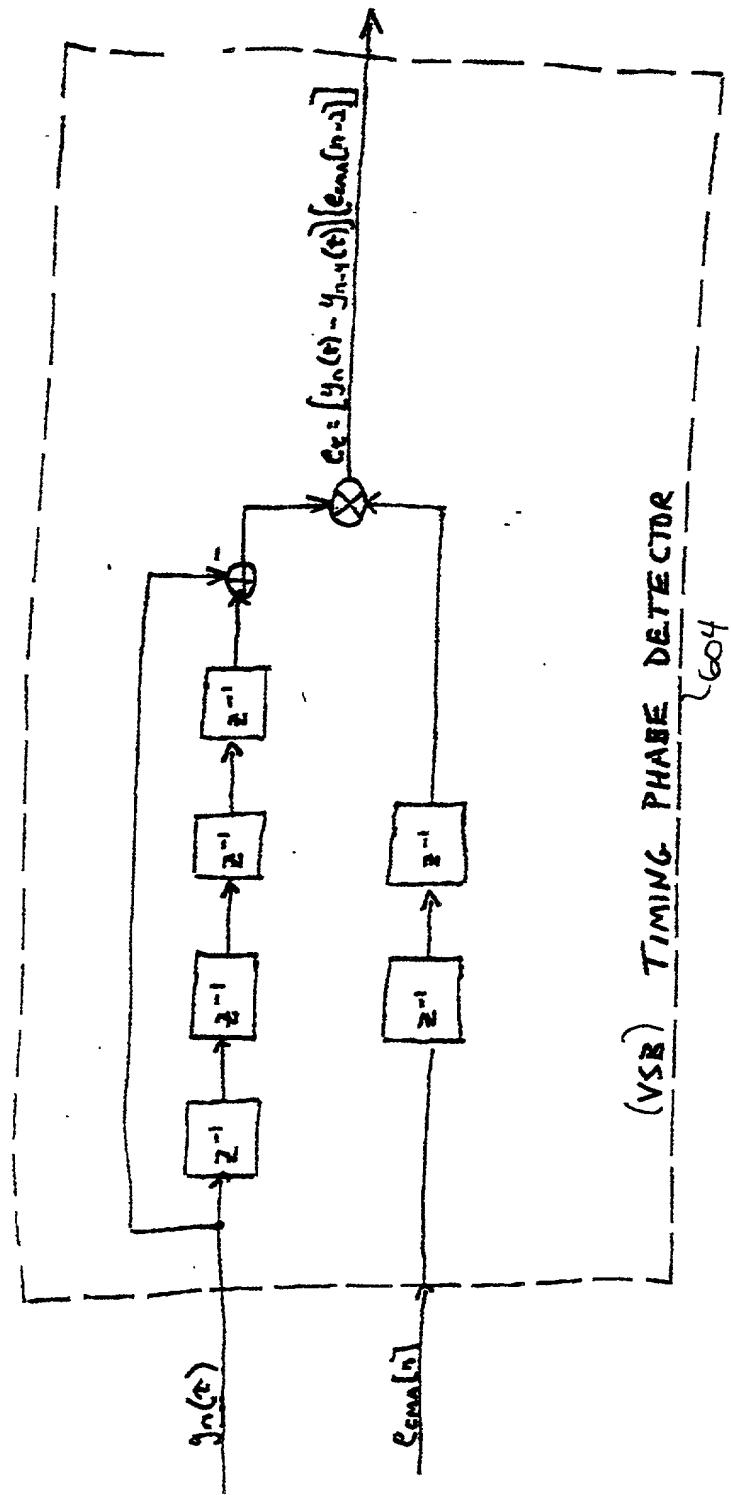


FIG. 11

FIGURE 10



Y<sub>n</sub>(t) = Y<sub>n-1</sub>(t) + Y<sub>n-2</sub>(t) + ... + Y<sub>1</sub>(t) + Y<sub>0</sub>(t)



(VSB) TIMING PHASE DETECTOR  
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FIGURE 12

Fig. 13

